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Examiner: Tan X. Dinh

Group: 2653

OFFICIAL

Date: March 1, 2004

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From: James M. Smith, Esq.

Subject: Paper: Foreign Priority Claim under 35 U.S.C. 119

Docket No.: 2037.1002-003

Applicants: Richard C. Foss, *et al.*

Serial No.: 10/032,431

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MDJ
3-3-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard C. Foss, Peter B. Gillingham, Robert Harland, Masami Mitsuhashi
and Atsushi Wada

Application No.: 10/032,431 Group: 2653

Filed: December 21, 2001 Examiner: Tan X. Dinh

Confirmation No.: 8755

For: DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT
ISOLATING TRANSISTORS

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or is being facsimile transmitted to the United States Patent and Trademark Office on:	
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FOREIGN PRIORITY CLAIM UNDER 35 U.S.C. §119

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned thanks Examiner Dinh for calling the undersigned regarding the priority claim of the original patent. Contrary to the statement made in the Amendment mailed December 10, 2002, foreign priority is being claimed under 35 U.S.C. §119 to the following foreign applications:

GB 9007789, filed April 6, 1990

GB 9107164, filed April 5, 1991

This claim was indicated on the copy of the face page of the original application 5,414,662 included as part of this reissue application.

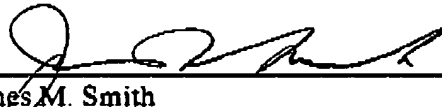
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In lieu of including the foreign priority information in the declaration, it is included in the attached Application Data Sheet in accordance with 37 C.F.R. 1.63(c) and 1.76 and MPEP 1417 at page 1400-36, left column, second paragraph.

The certified copy of the priority document was filed in the parent application, U.S. Application No. 08/853,507, filed May 8, 1997.

Respectfully submitted,

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By 
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